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Wipro Placement Papers 2020 and Questions with Answers ...
1) Explain how logical gates are controlled by Boolean logic? In Boolean algebra, the true state is denoted by the number one, referred as logic one or logic high. While, the false state is represented by the number zero, called logic zero or logic low. And in the digital electronic, the logic high is denoted by the presence of a voltage potential.

Top 17 VLSI Interview Questions & Answers
Qualcomm Written Test Overview. Qualcomm had changed its curriculum in May 2019. Here you can find the test pattern of Qualcomm Placement paper. In Qualcomm, there is 0.25 negative marking in the Qualcomm placement paper. Qualcomm Placement Papers have the following sections in the exam - Total Number of Questions - 60 Questions; Total Time ...

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