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Bipolar Junction Transistor (BJT) Basics- GATE Problems One Mark Questions 1. The break down voltage of a transistor with its base open is BV_{CEO} and that with emitter open is BV_{CBO} , then (a) $BV_{CEO} = BV_{CBO}$ (b) $BV_{CEO} > BV_{CBO}$ (c) $BV_{CEO} < BV_{CBO}$ (d) BV_{CEO} is not related to BV_{CBO} [GATE 1995] Soln. The given voltage ratings are reverse breakdown voltages. BV_{CEO}

Bipolar Junction Transistor (BJT) Basics- GATE Problems

c) the input signal is connected in series with the voltage applied to bias the base-emitter junction. d) the input signal is connected in series with the voltage applied to

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bias the base-collector junction.

Ans.(a), (c) When a transistor is used in the common emitter mode as an amplifier, then the options (a) and (c) are correct.

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Question 1. Explain the biasing of a transistor. Answer: The application of suitable DC voltage across the transistor terminals is called biasing of the transistor.

The reasons for biasing of a transistor are:

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